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AUG 03 2005

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of

Kuzemchak et al. (TI-32443)

Conf. No. 6142

Serial No. 10/017,777

Group Art Unit: 2192

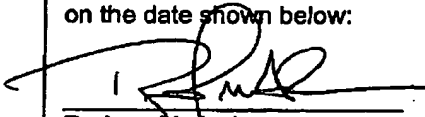
Filed: December 13, 2001

Examiner: Vo

For: Method and Tool for Verification of Algorithms Ported From One Instruction Set  
Architecture to Another

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**FACSIMILE COVER SHEET**

☒ **FACSIMILE COVER SHEET**  
☐ NEW APPLICATION  
☐ DECLARATION  
☐ ASSIGNMENT  
☐ FORMAL DRAWINGS  
☐ INFORMAL DRAWINGS  
☐ CONTINUATION APP'N  
☐ DIVISIONAL APP'N

☒ **AMENDMENT UNDER RULE 116**  
**(15 Pages)**  
☐ EOT  
☒ **NOTICE OF APPEAL (1 Page) (dupl.)**  
☐ **APPEAL**

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Texas Instruments Incorporated  
PO Box 655474, M/S 3989  
Dallas, TX 75074

**RESPONSE UNDER 37 C.F.R. 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2192**

PATENT

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**AMENDMENT UNDER RULE 116**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

**Introductory Comments:**

This paper is presented in response to the Office Action mailed on May 3, 2005. It is submitted that this paper places all claims in condition for allowance, or alternatively places this case in better condition for appeal. Entry of this amendment in, and reconsideration of, this application are respectfully requested.

Amendment to the claims begins on page 2.

Applicants' remarks begin on page 10.